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CLAIMS

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- 0325.00420
1. An apparatus comprising:
a circuit configured to select one of a number of identification (ID) codes in response a voltage level at one or more pins.
 2. The apparatus according to claim 1, wherein said ID code is a silicon ID of an electronic part.
 3. The apparatus according to claim 1, wherein said number of ID codes are programmed using one or more options selected from the group consisting of metal options, bond options, and hard coded options.
 4. The apparatus according to claim 1, wherein said one or more pins are connected to either a voltage supply power or a voltage supply ground according to package markings.
 5. The apparatus according to claim 1, wherein said ID code comprises a part number for said apparatus.

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6. The apparatus according to claim 5, wherein said ID code is combined with other identification codes.

7. The apparatus according to claim 6, wherein said other ID codes comprise one or more codes selected from the group consisting of a version number and a manufacture number.

8. The apparatus according to claim 1, wherein said ID code is captured in a register.

9. The apparatus according to claim 8, wherein said register comprises a JTAG ID code register.

10. The apparatus according to claim 1, wherein said apparatus comprises a programmable logic device (PLD).

11. The apparatus according to claim 1, wherein said metal options are set to indicate an operating voltage of said apparatus.

12. The apparatus according to claim 1, wherein said bond options are set based on a package of said apparatus.

13. The apparatus according to claim 1, wherein said pins are labeled as either a first or a second supply voltage.

14. The apparatus according to claim 13, wherein said pins are labeled as either said first or said second supply voltage based on characteristics of said apparatus.

15. The apparatus according to claim 14, wherein said characteristics comprise one or more characteristics selected from the group consisting of volatility, price, and density.

16. A method of selecting one of a number of identification codes for a single die and package combination comprising the steps of:

- (A) asserting a first or a second voltage level on one or more pins of said package; and
- 5 (B) generating said one of a number of identification codes in response to said voltage levels on said one or more pins.

17. The method according to claim 16, wherein the step
(B) comprises the sub-steps of:

(B-1) determining said voltage levels;

(B-2) determining a state of one or more metal
5 options;

(B-3) determining a state of one or more bond
options; and

(B-4) generating an identification code in response
to a logical combination of the determinations of sub-steps (B-1)
through (B-2)

18. The method according to claim 16, further comprising
the step of:

(C) presenting said identification code in response to
an identification request.

19. The method according to claim 18, wherein said
identification request comprises a JTAG ID code instruction.

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20. An apparatus comprising:

means for generating a number of identification codes in response to a voltage level asserted at one or more pins; and

means for determining a voltage level to assert at one or
5 more pins.